## TRANSMITTAL FORM . . . .

Attorney Docket No.

RPS920010132US1

2285P

TRADE The application

BALLIS et al.

Confirmation No: 5851

Serial No: **10/015,921** 

Group Art Unit: 2825

Filed: December 10, 2001

Examiner: Dinh, Paul

For: Method and System for use of a Field Programmble Gate Array (FPGA) Cell for Controlling Access to On-Chip Functions of a System on a Chip Integrated

to On-Chip Functions of a System on a Chip integrated								
ENCLOSURES (check all that apply)								
	Amendment/Reply			Assignment and Recordation Cover Sheet			After Allowance Communication to Group	
	After Final			Part B-Issue Fee Transmittal			Notice of Appeal	
	Information disclosure statement			Letter to Draftsman			Appeal Brief	
	Form 1449			Drawings			Status Letter	
	(X) Copies of References			Petition			Postcard	
	Extension of Time Request *			Fee Address Indication Form			Other Enclosure(s) (please identify below):	
	Express Abandonment			Terminal Disclaimer				
	Certified Copy of Priority Doc			Power of Attorney an Revocation of Prior P				
	Response to Inco	mplete Appln		Change of Correspondence Address				
	Response to Missing Parts  *Extension of Term: Pursuant to 37 CFR 1.136, Applicant petitions							
	Executed D Inventor(s)	Declaration by		Commissioner to extend the time for response for xxxxxx month(s), from to.				
	FOR	Claims Remain	CLAIMS ing   Highest # of Claims   Extra Cla			aims	RATE	FEE
After Amendme			_	Previously Paid For	Extra olamo			
Total Claims		19		20 3	0		\$ 50.00	\$ 0.00 \$ 0.00
Independent Claims		3		3   0			\$200.00 Total Fees	\$ 0.00
METHOD OF PAYMENT								
Check no in the amount of \$ is enclosed for payment of fees.								
	Charge \$ to Deposit Account No (Account Holder Name) for payment of fees.							
Charge any additional fees or credit any overpayment to Deposit Account No. <u>50-0563</u> (IBM Corporation)								
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT								
Attorney Name Joseph A. Sawyer, Jr., Reg. No. 30,801								
Signature Joseph G Am								
Date								
CERTIFICATE OF MAILING								
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on June 29, 2005								
	or printed name	Saundra Do Hu	nter _		<del></del>			
Signature 903km/01								





## **CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Mail Stop Amendment, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on June 29, 2005.

Saundra D. Hunter

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: June 29, 2005

BAILIS, et al

Confirmation No. 5851

Serial No. 10/015,921

Group Art Unit: 2825

Filed:

December 10, 2001

Examiner: Dinh, Paul

For: METHOD AND SYSTEM FOR USE OF A FIELD PROGRAMMABLE GATE ARRAY (FPGA) CELL FOR CONTROLLING ACCESS TO ON-CHIP FUNCTIONS OF A SYSTEM ON A CHIP (SOC) INTEGRATED CIRCUIT

Mail Stop Amendment Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

## **AMENDMENT**

Sir:

In response to the Office Action dated March 29, 2005, please enter the following amendments and remarks in the present Application:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 8 of this paper.